

FIG. 1

FIG. 2 is a block diagram of a system 200 according to one embodiment of the present invention. The system 200 includes a TLC SRAMS 8/16MB 48, a TLC Controller 26, and a plurality of Voyager_{LP} devices 50, 52, 54, 56. The TLC SRAMS 48 is connected to the TLC Controller 26. The TLC Controller 26 is connected to the Voyager_{LP} devices 50, 52, 54, 56. The Voyager_{LP} devices 50, 52 are grouped together in a dashed box 28, and the Voyager_{LP} devices 54, 56 are grouped together in a dashed box 30.

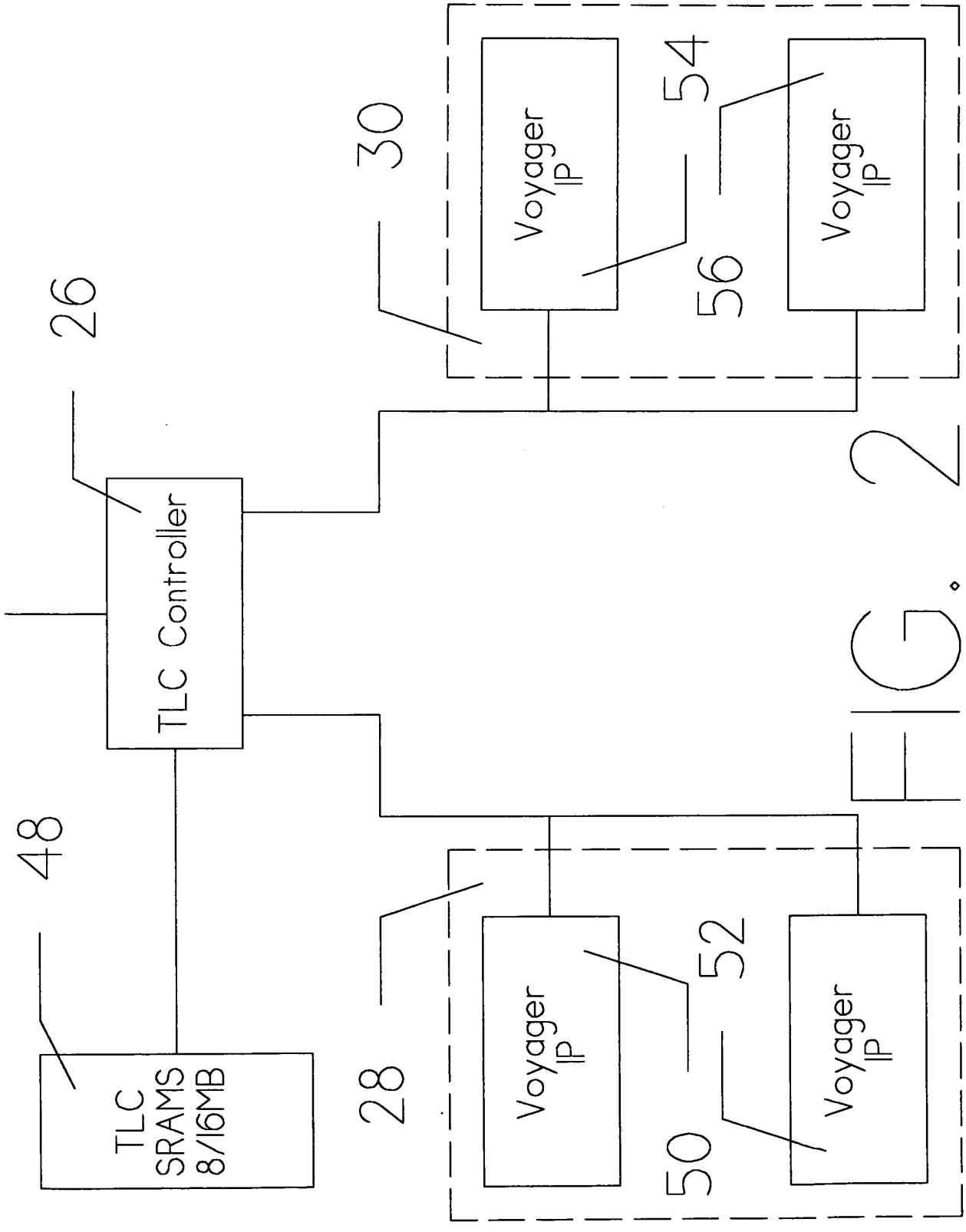
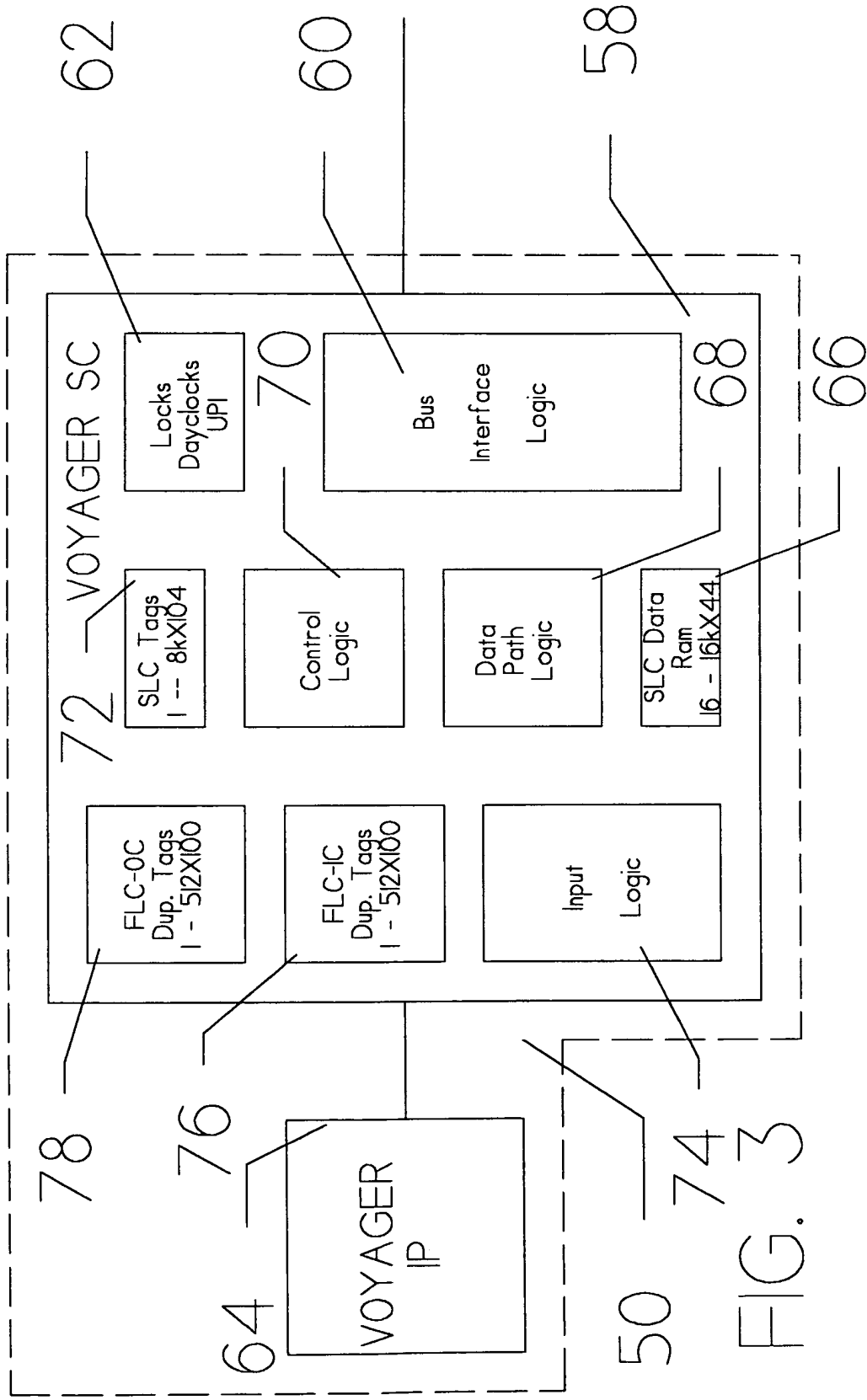


FIG. 2



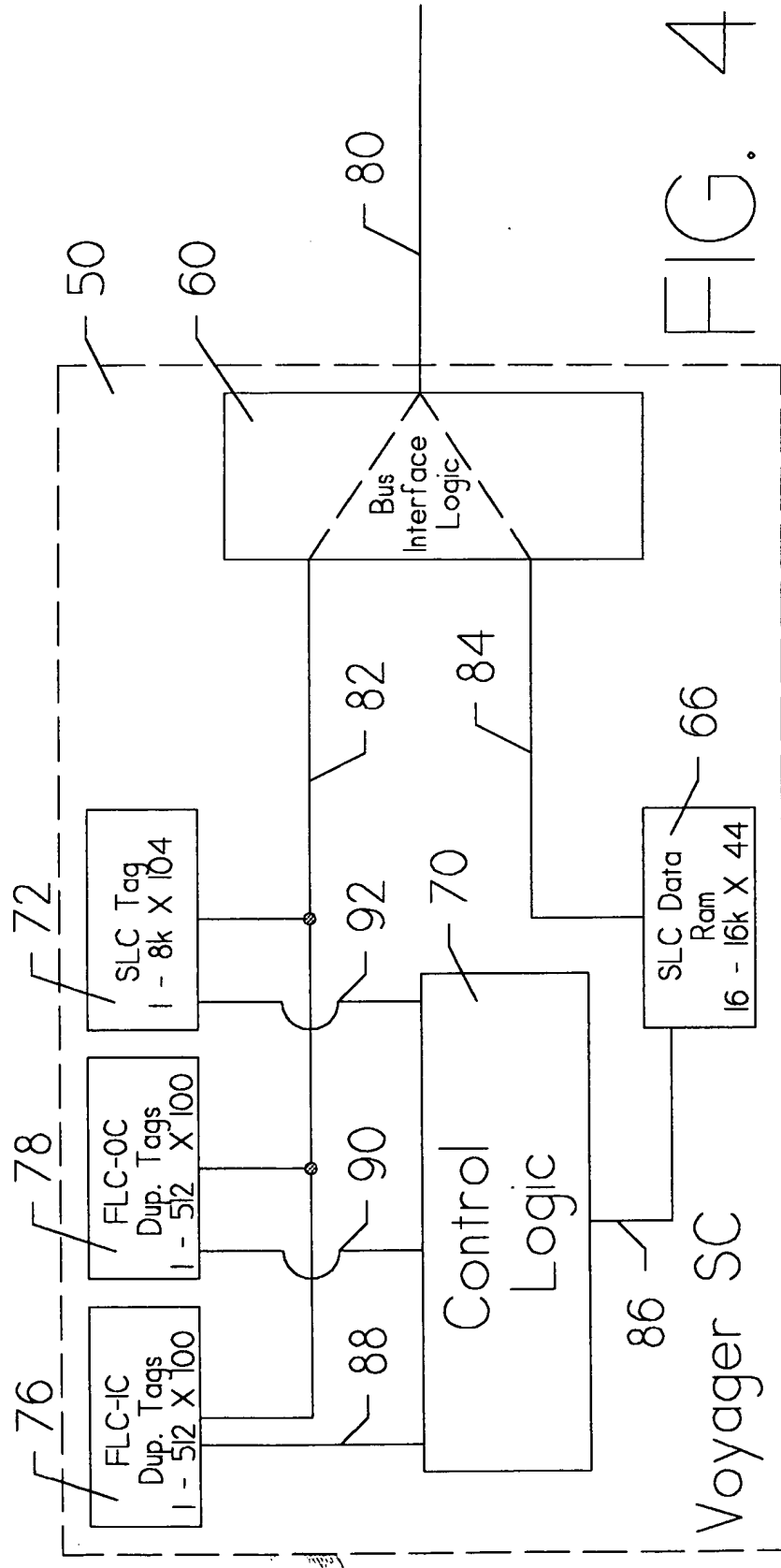


FIG. 4

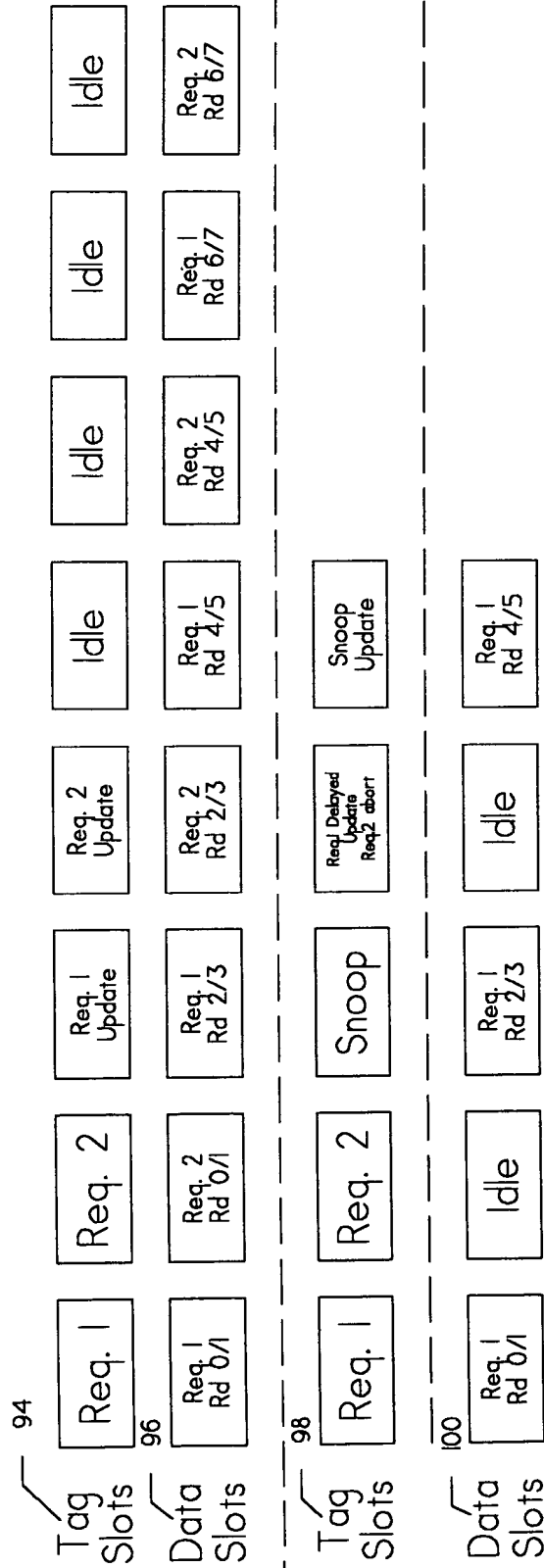


FIG. 5